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Term	Documents
HARVARD.DWPI,TDBD,EPAB,JPAB,USPT,PGPB.	6412
HARVARDS	0
(HARVARD AND 3).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	140
(HARVARD AND L3).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	140

Database:

US Patents Full-Text Database
 US Pre-Grant Publication Full-Text Database
 JPO Abstracts Database
 EPO Abstracts Database
 Derwent World Patents Index
 IBM Technical Disclosure Bulletins

Search:

L4

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 DATE: Tuesday, March 12, 2002 [Printable Copy](#) [Create Case](#)

<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L4</u>	harvard and l3	140	<u>L4</u>
<u>L3</u>	(@pd<19981029) and l2 and l1	3500	<u>L3</u>
<u>L2</u>	data adj (cache or memory)	114648	<u>L2</u>
<u>L1</u>	instruction adj (cache or memory)	12032	<u>L1</u>

END OF SEARCH HISTORY

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Term	Documents
(15 AND 16).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	1
(L16 AND L15).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	1

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<u>Set Name</u> side by side	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u> result set
<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>			
<u>L17</u>	l16 and l15	1	<u>L17</u>
<u>L16</u>	(cache or memory) adj controller	23849	<u>L16</u>
<u>L15</u>	5829031.pn.	3	<u>L15</u>
<u>L14</u>	l12 and l13 and l3	84	<u>L14</u>
<u>L13</u>	(data) adj2 (prefetch or prefetching)	769	<u>L13</u>
<u>L12</u>	(instruction) adj2 (prefetch or prefetching)	1580	<u>L12</u>
<u>L11</u>	l10 and l3	402	<u>L11</u>
<u>L10</u>	l4 same instruction same data	1805	<u>L10</u>
<u>L9</u>	((@pd<19981029) and l7	36	<u>L9</u>
<u>L8</u>	l7 and l6	0	<u>L8</u>
<u>L7</u>	harvard architecture same advantages	44	<u>L7</u>
<u>L6</u>	l4 and l3	664	<u>L6</u>
<u>L5</u>	L4	5569	<u>L5</u>
<u>L4</u>	(prefetch or prefetching)	5569	<u>L4</u>
<u>L3</u>	((@pd<19981029) and l1 and l2	3500	<u>L3</u>
<u>L2</u>	data adj (cache or memory)	114648	<u>L2</u>
<u>L1</u>	instruction adj (cache or memory)	12032	<u>L1</u>

END OF SEARCH HISTORY